

## Applicable Country & Regions:

All Regions

# Product Service Manual – Level 3

Service Manual for BenQ:  
Projector/  
W1070, W1080ST  
< 9H.J7L77.17X, 9H.J7M77.17X >



Version: 00a  
Date:2012/10/04

### Notice:

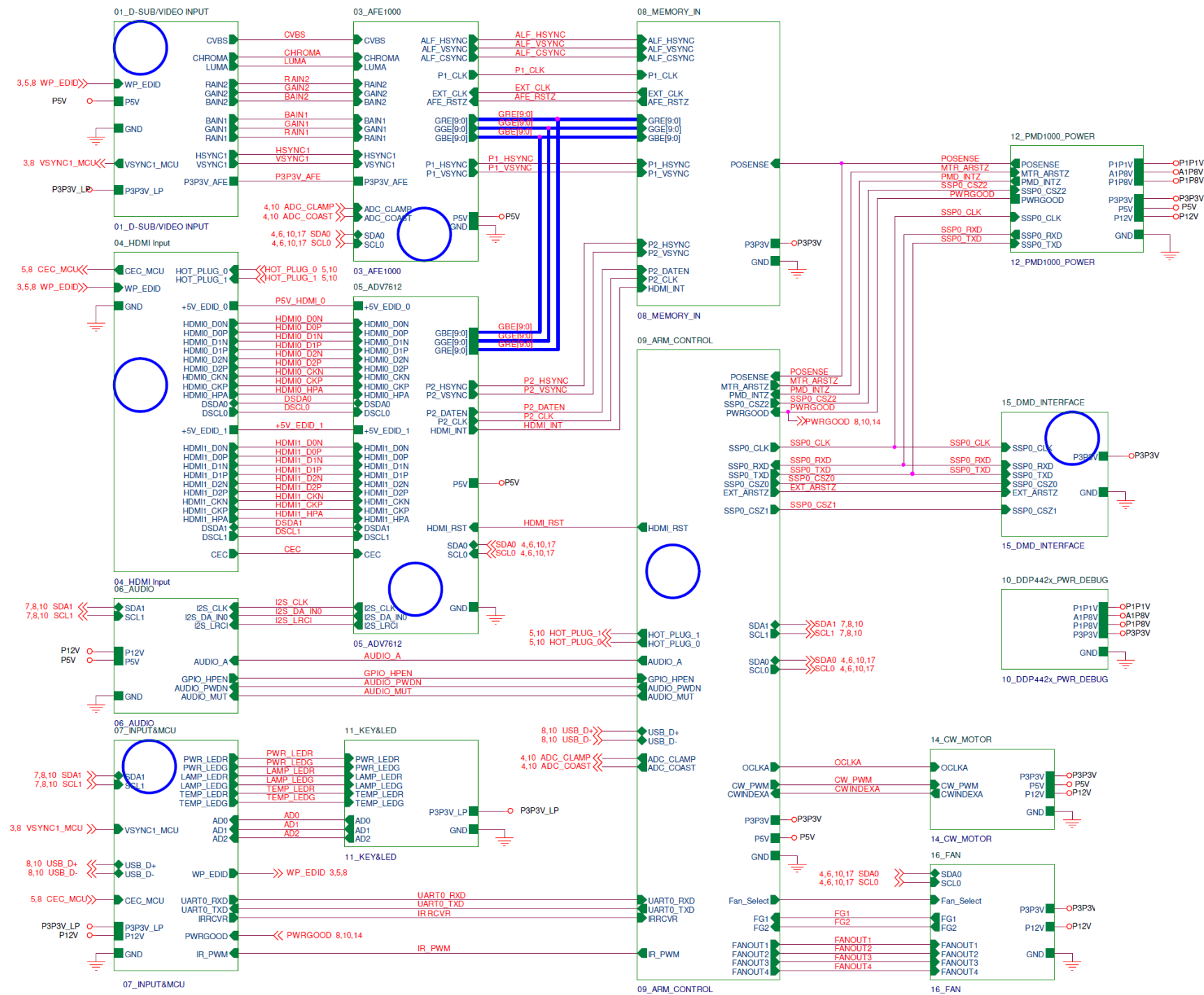
For RO to input specific “Legal Requirement” in specific NS regarding to responsibility and liability statements.

*Please check BenQ's eSupport web site, <http://esupport.benq.com>, to ensure that you have the most recent version of this manual.*

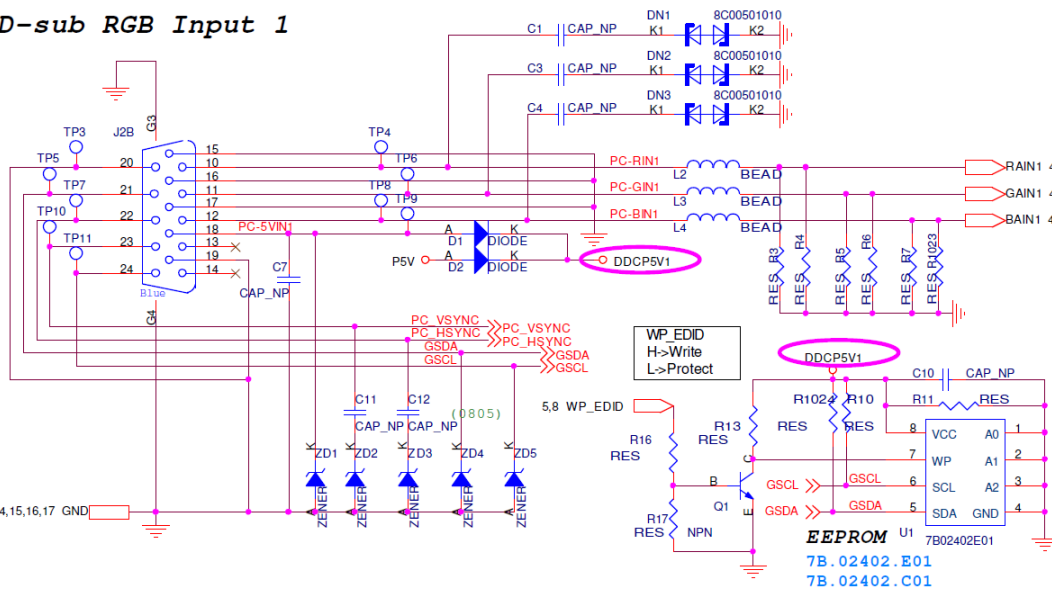
First Edition (Oct. 2012)

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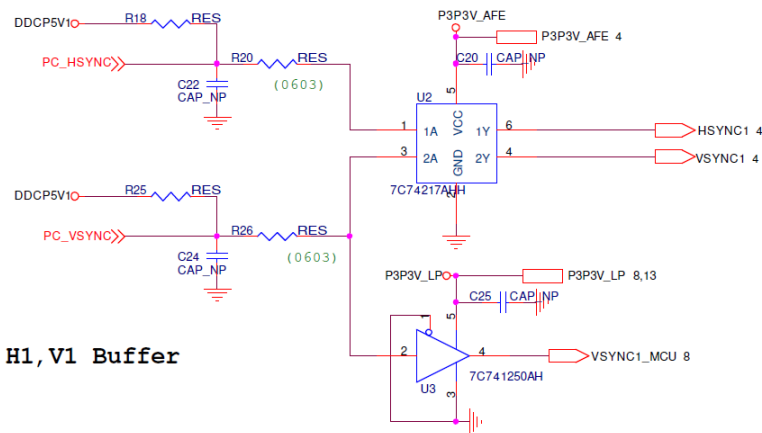
Schematics—Main board:



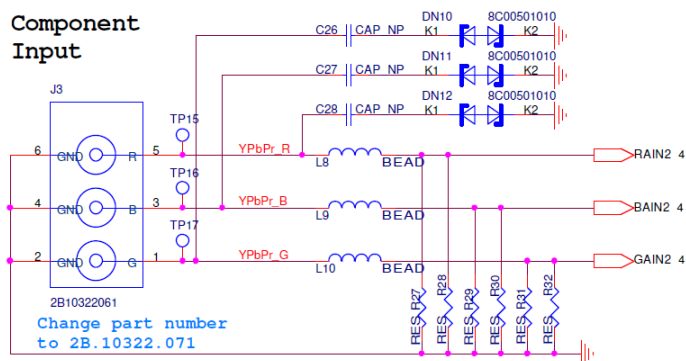
## D-sub RGB Input 1



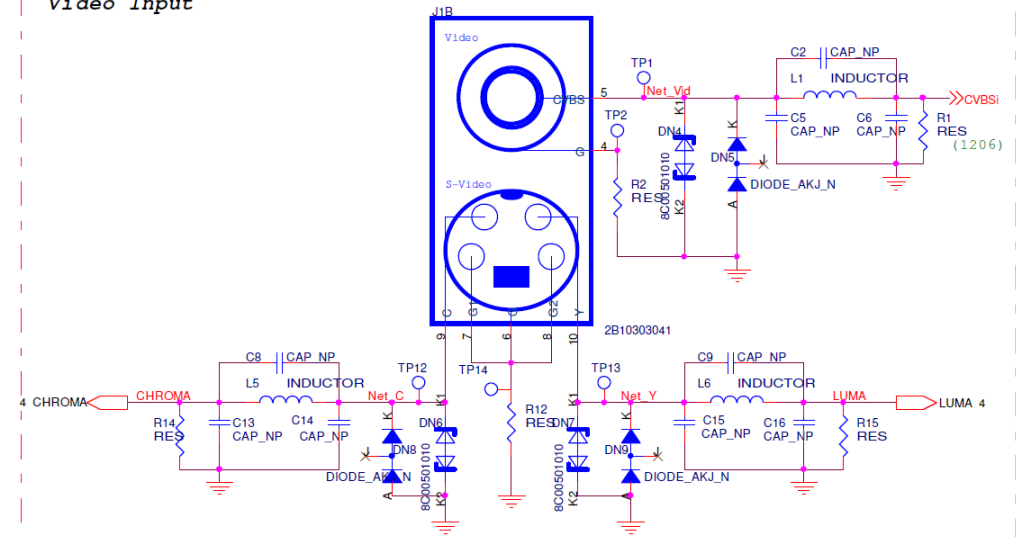
## H1,V1 Buffer



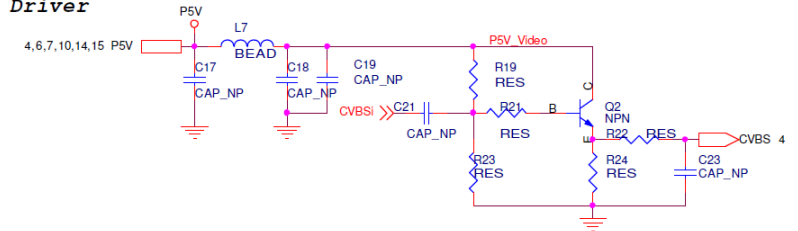
## Component Input

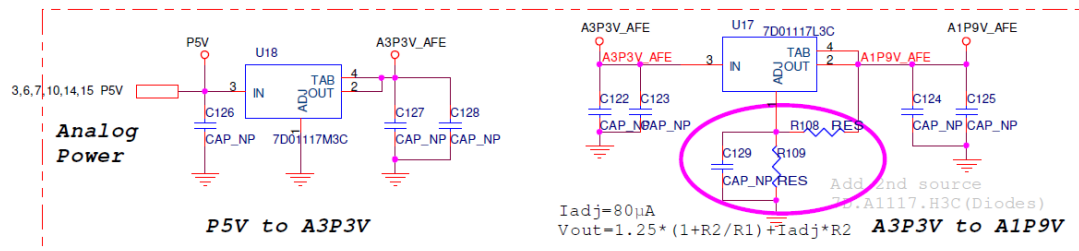
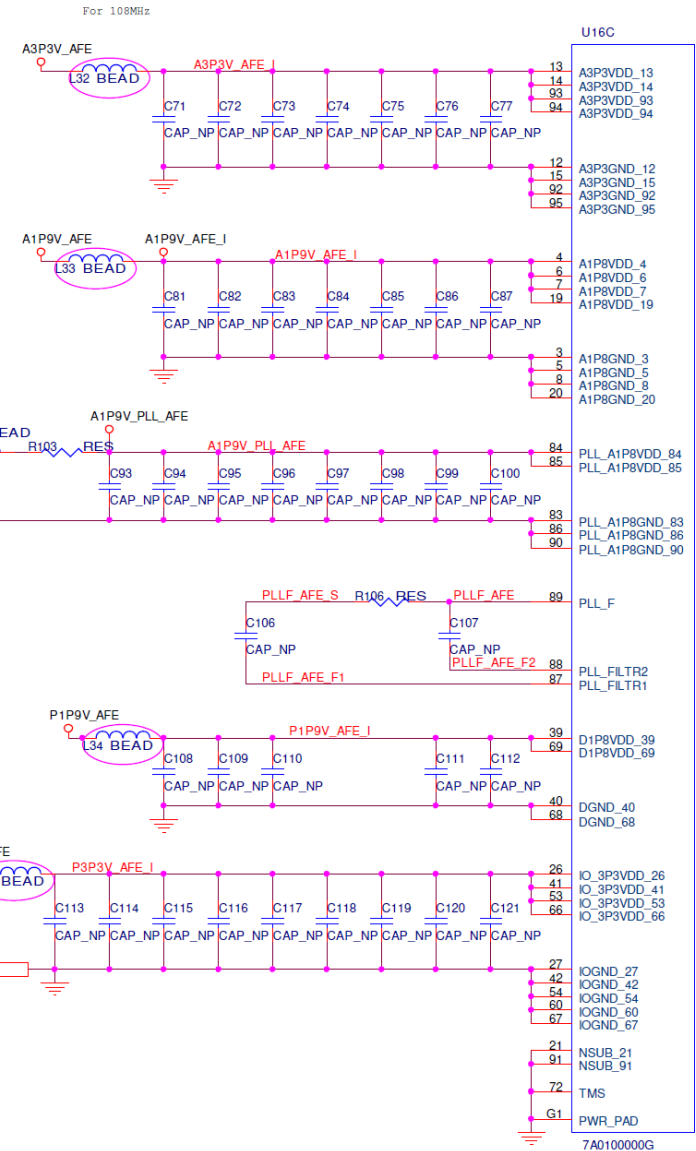
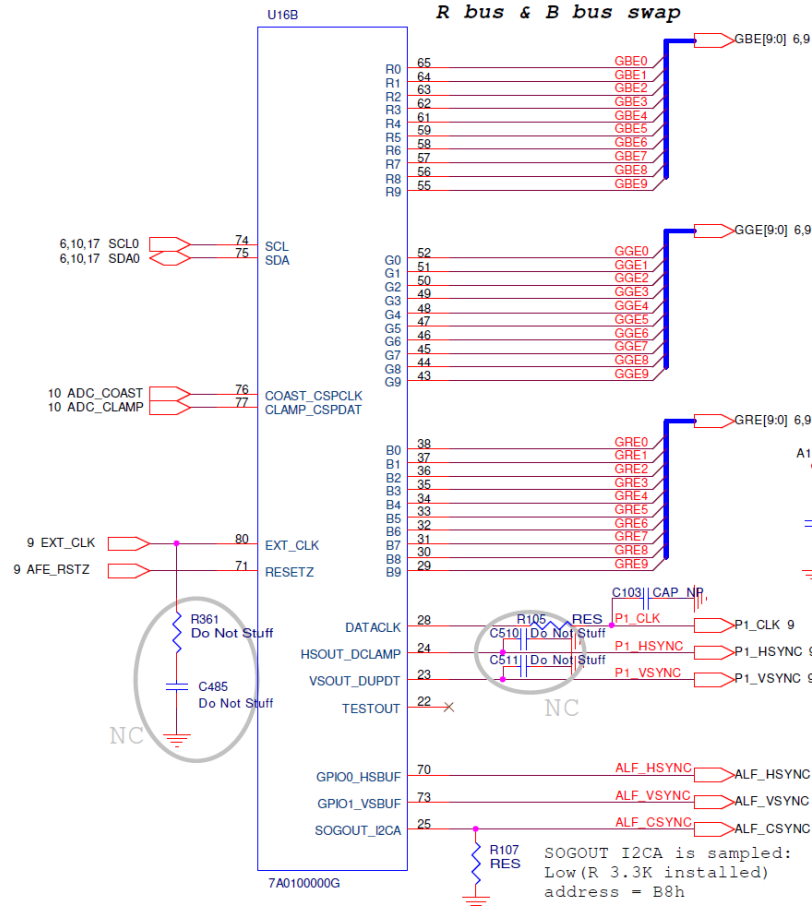
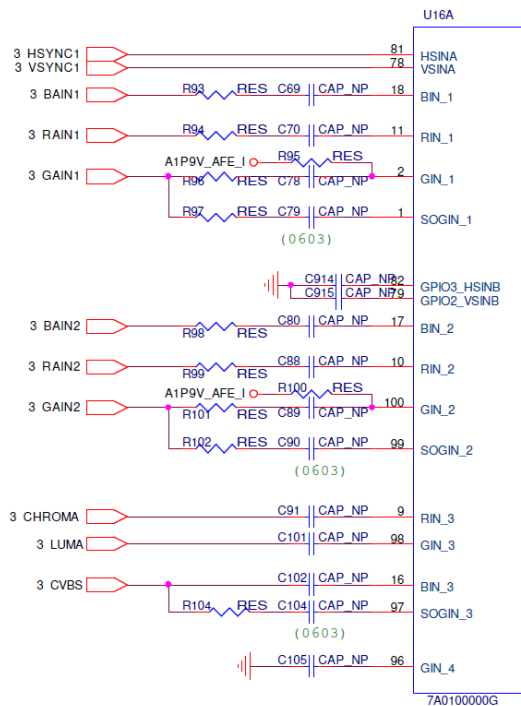


## Video Input



## Video Signal Driver



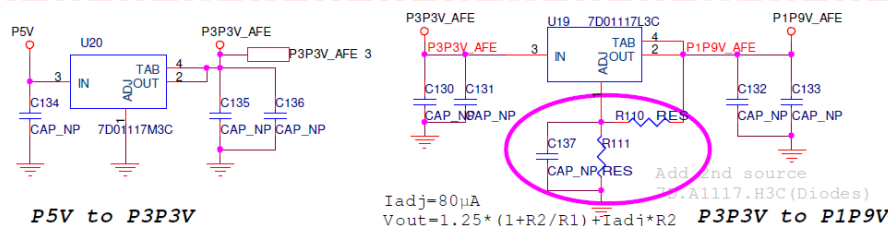


$$I_{adj}=80\mu A$$

$$V_{out}=1.25 * (1+R2/R1) + I_{adj} * R2$$

A3P3V to A1P9V

Digital Power



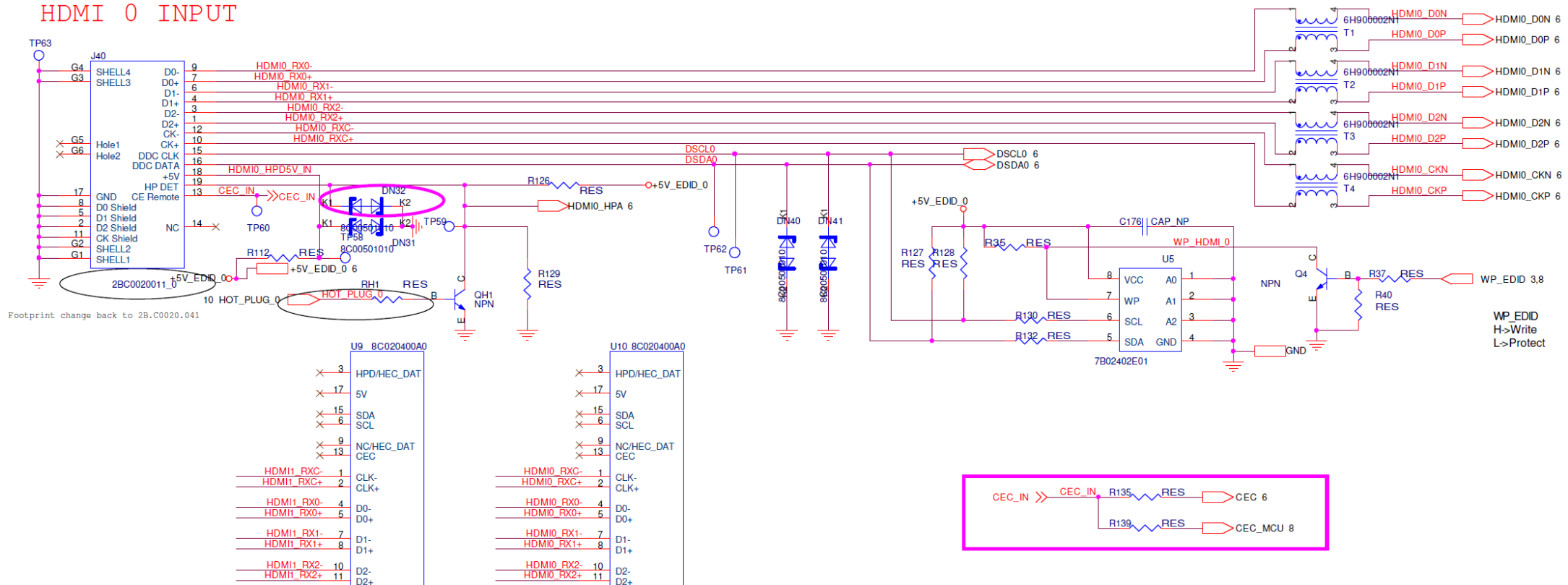
$$I_{adj}=80\mu A$$

$$V_{out}=1.25 * (1+R2/R1) + I_{adj} * R2$$

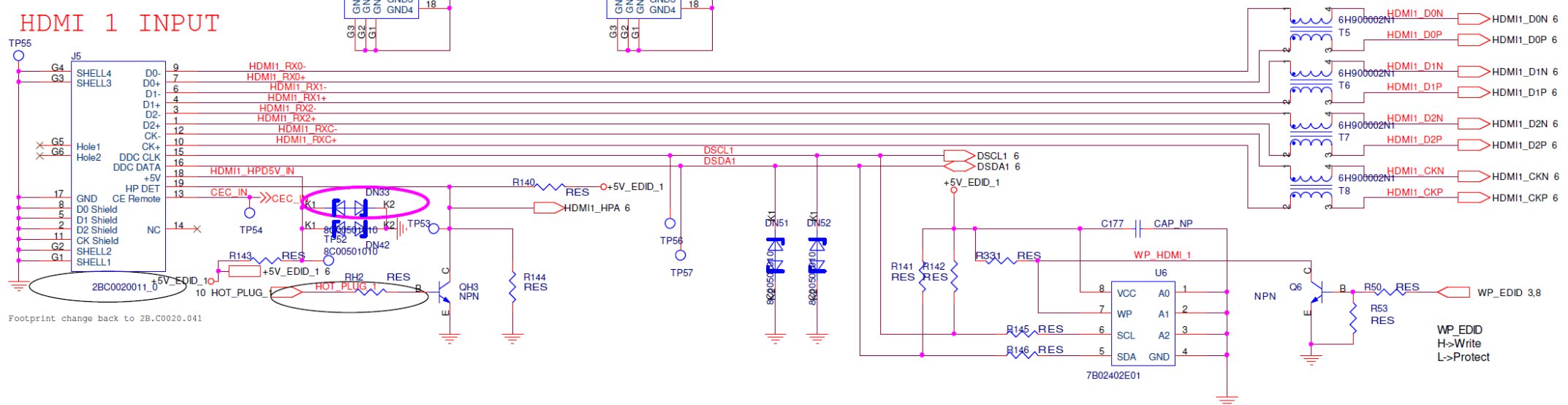
P3P3V to P1P9V

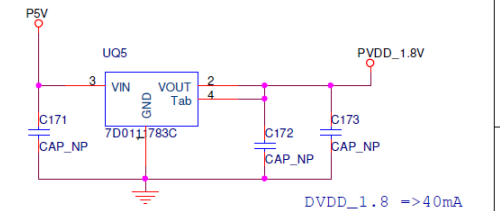
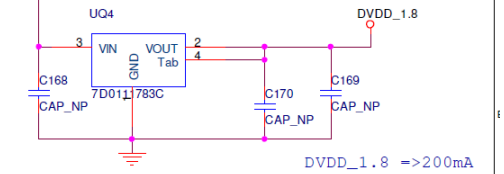
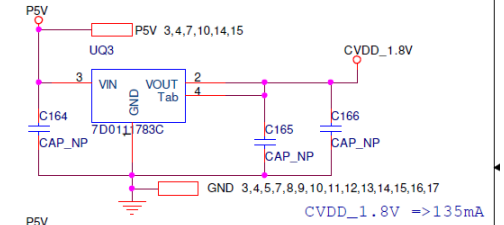
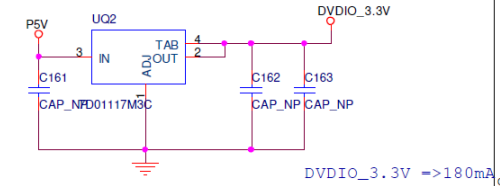
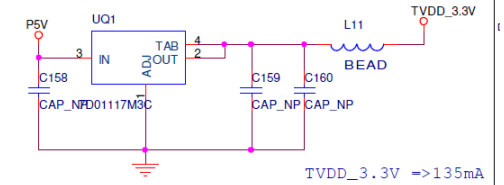
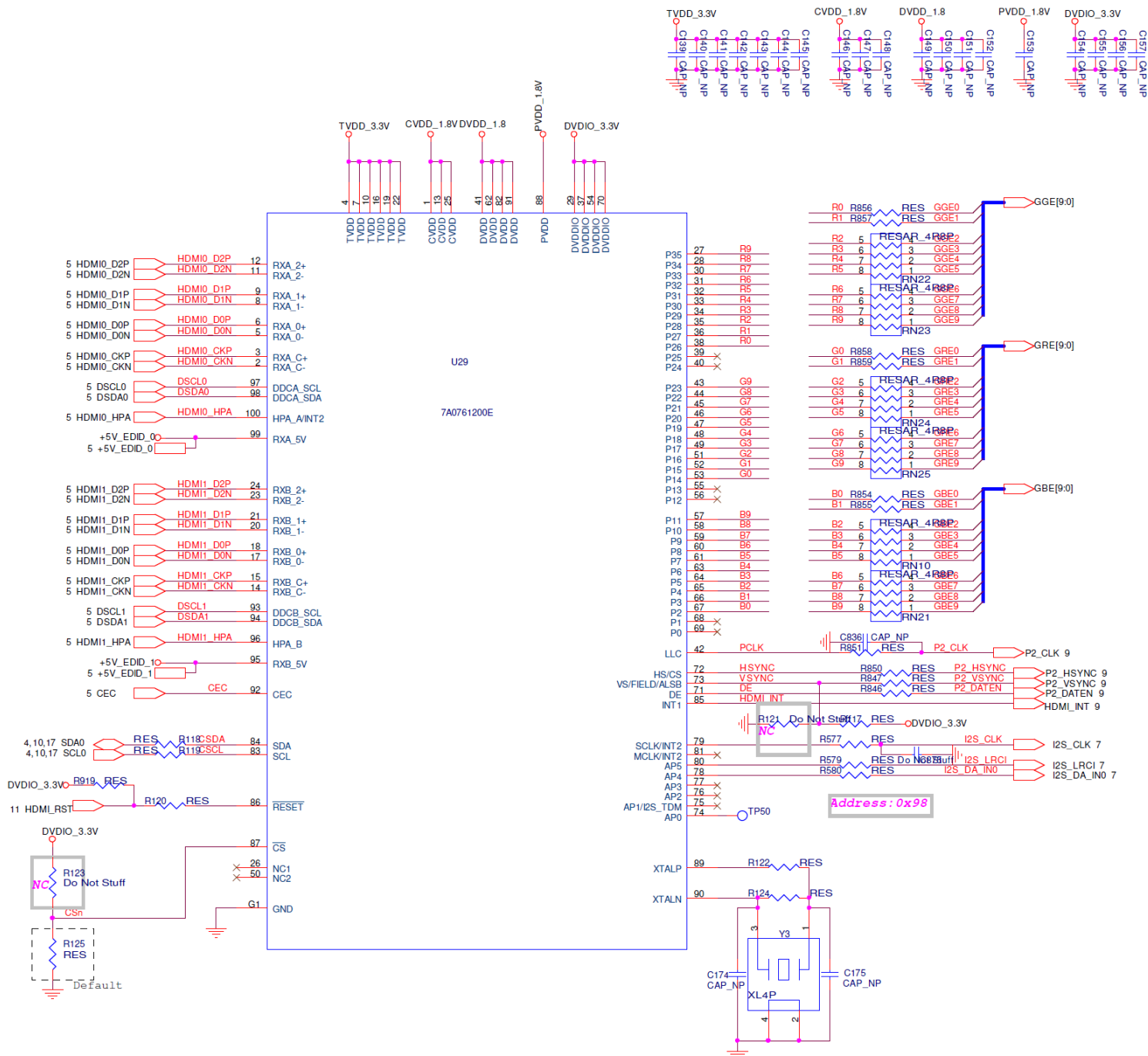


## HDMI 0 INPUT



## HDMI 1 INPUT





HPEN

H: AudioOut ON  
L: AudioOut OFF

U95

7C741250AH

C982  
CAP\_NP

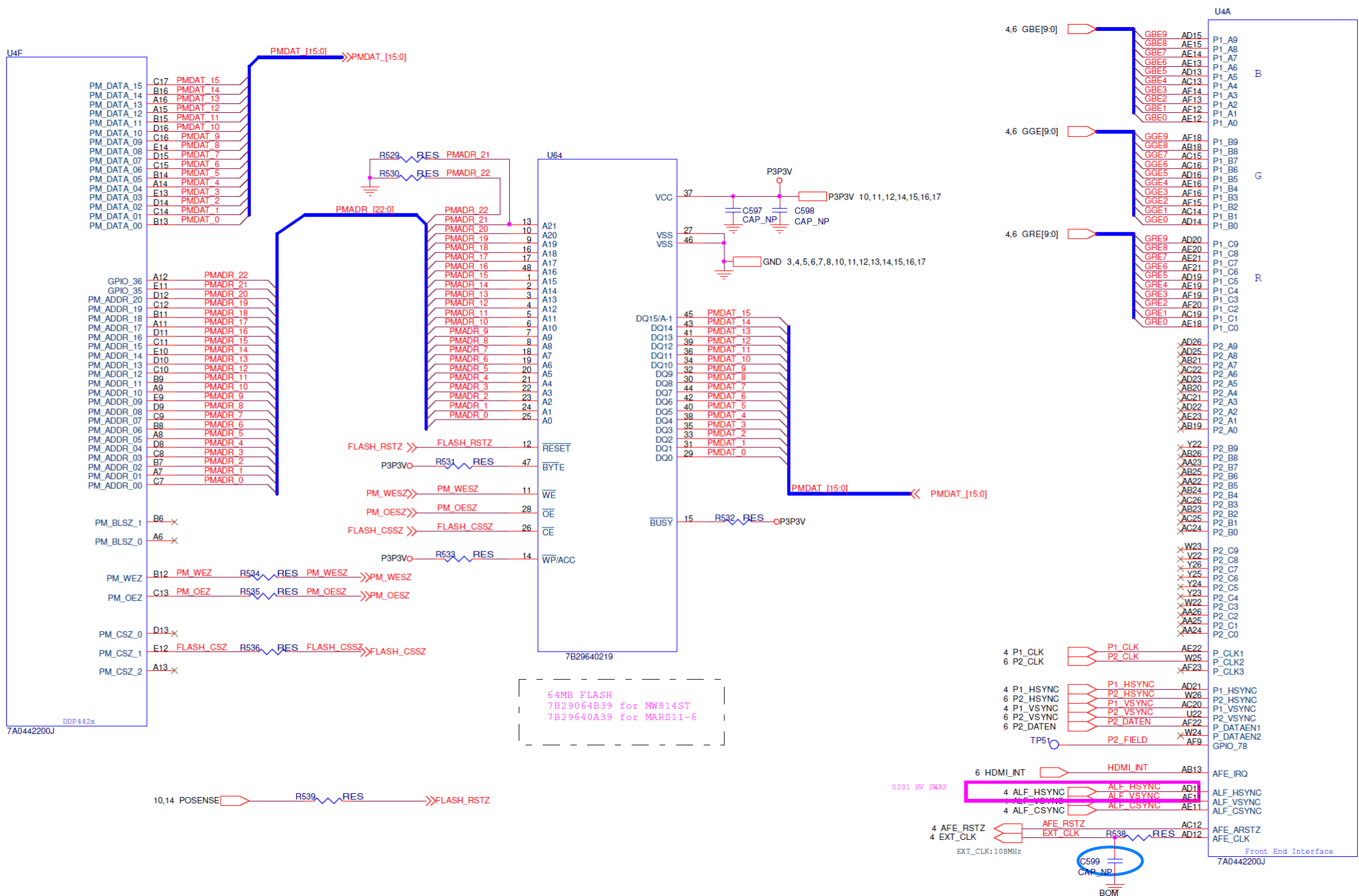
R958

RES

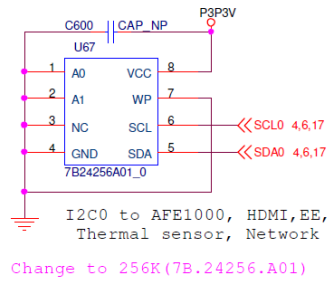
R961  
RES

GPIO\_HPEN

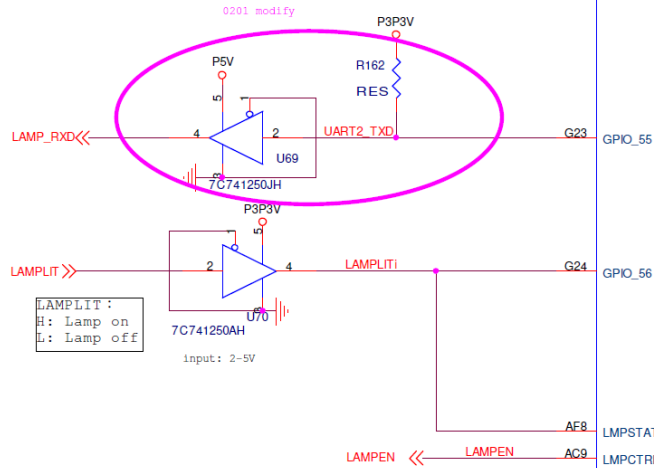




Address = 0xA0



I2C1 to MCU, AUDIO



UART1\_TXD C19  
UART1\_RXD D19  
X E18  
X A19

8 IRRVCVR IR0 X B26  
TP37 D24

LAMPEN << LAMPEN AC9

GPIO\_28  
GPIO\_29  
SCL0 A10  
SCL0 SDA0 B10  
SCL1 D20  
SCL1 SDA1 E19

GPIO\_39 F24  
GPIO\_38 E25 USB\_EN  
Control by Boot code, Don't move.

BT656 INTERFACE (GPIO40-50)

Fan\_Select 17

GPIO\_51 AC11 ADC\_COAST  
GPIO\_52 AB12 ADC\_CLAMP  
ADC\_COAST 4  
ADC\_CLAMP 4

GPIO\_53 E26 GPIO\_HPEN  
GPIO\_54 F26  
GPIO\_59 G26 AUDIO\_PWDN  
AUDIO\_PWDN 7

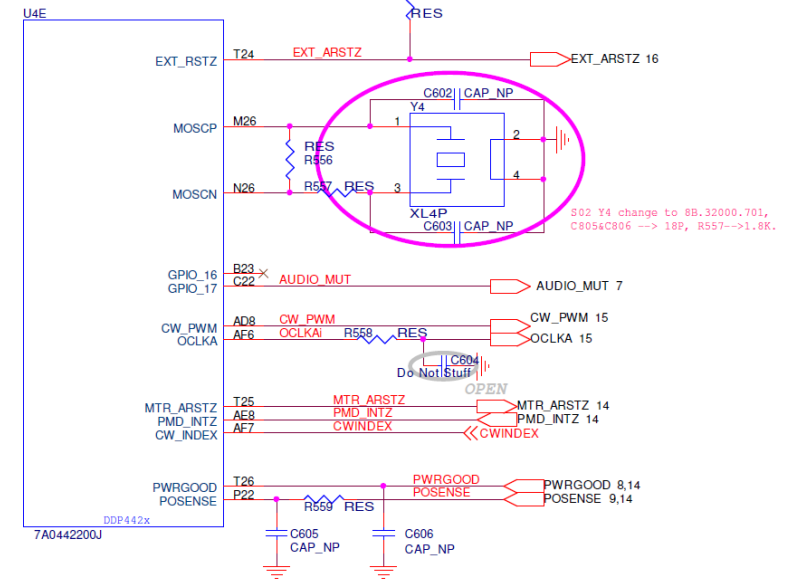
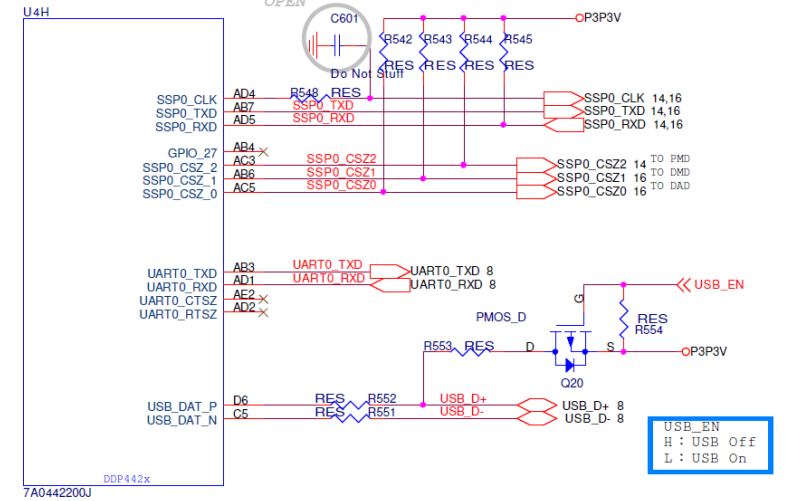
GPIO\_60 H22  
GPIO\_61 H23 TP35  
GPIO\_62 H24  
GPIO\_63 H25  
GPIO\_64 H26  
GPIO\_65 J22  
GPIO\_66 J23  
GPIO\_67 J24  
GPIO\_68 J25  
GPIO\_69 J26  
GPIO\_70 K22  
GPIO\_71 K23  
GPIO\_72 K24

LED LAMP CONTROL (GPIO68-72)

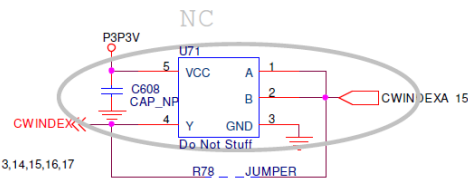
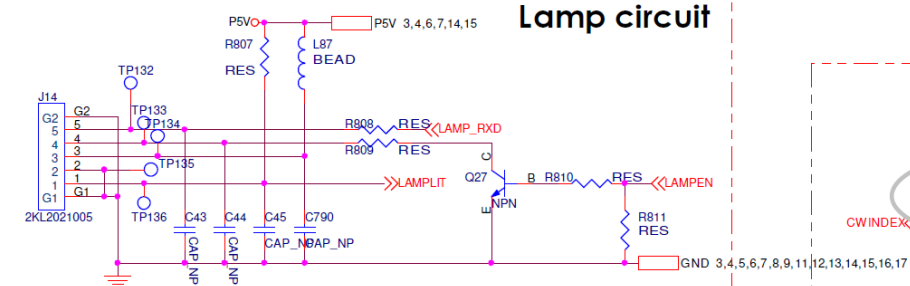
GPIO\_04 B19  
GPIO\_04 for DB

GPIO\_37 G22 TP36  
GPIO\_82 E3

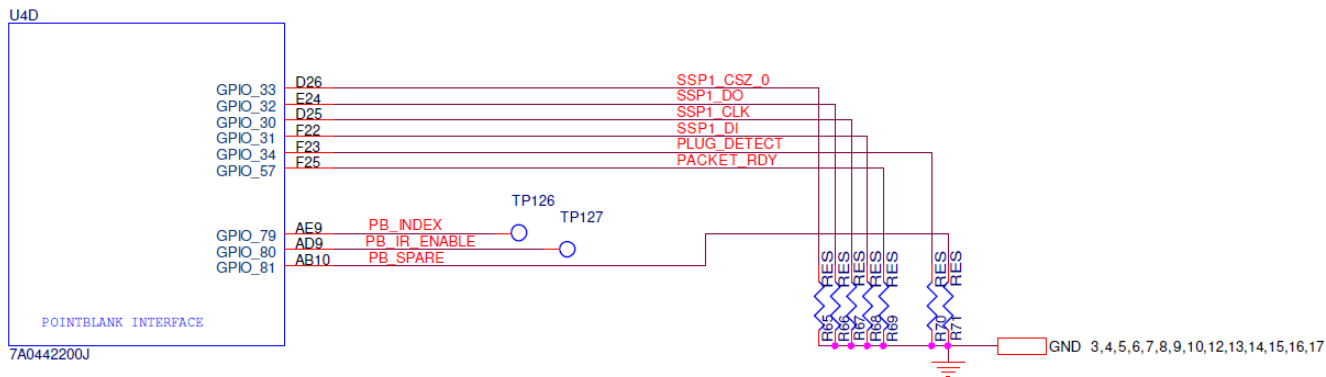
HOT\_PLUG\_1  
HOT\_PLUG\_0  
HOT\_PLUG\_1  
HOT\_PLUG\_0



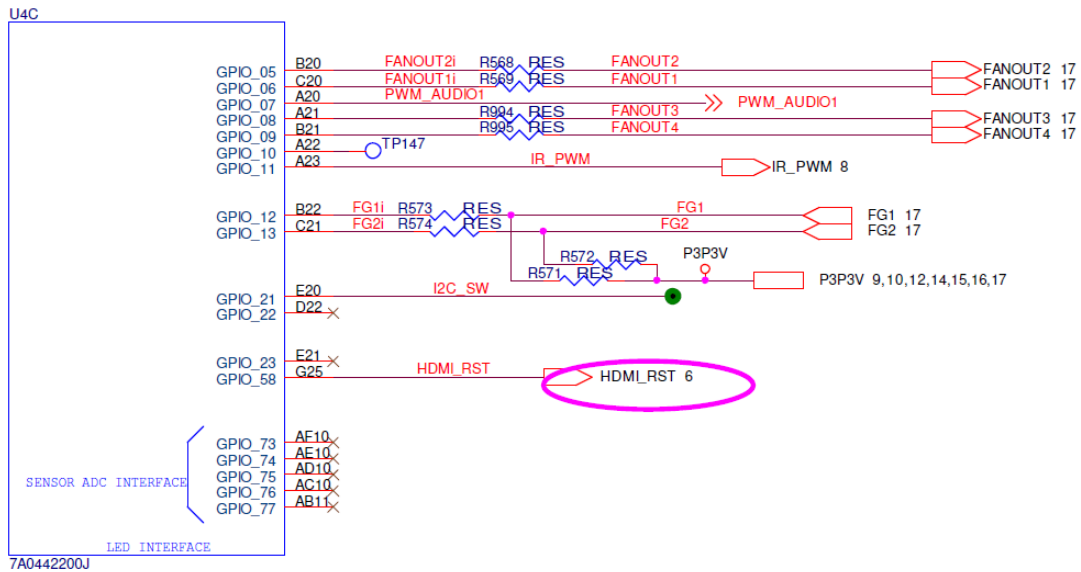
## Lamp circuit



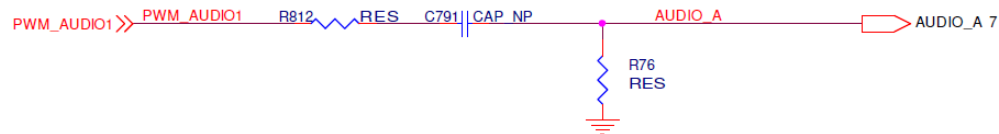




Connector for Radio Transceiver



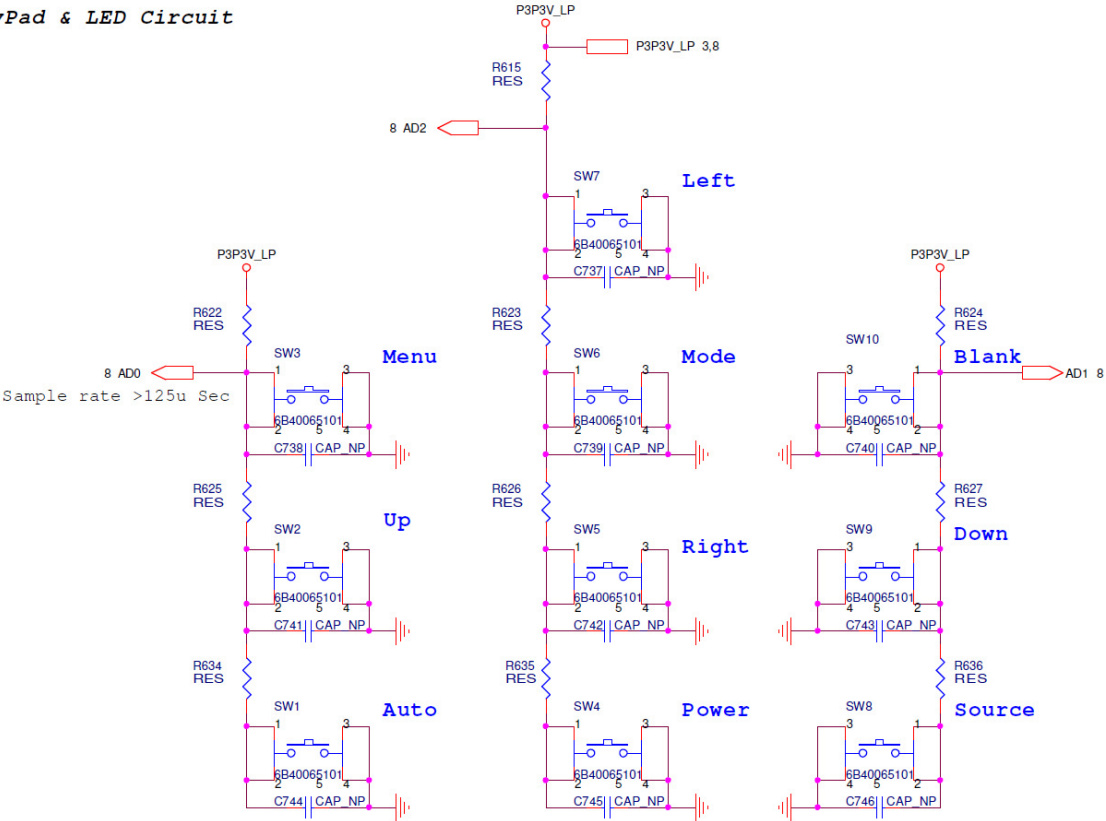
TurnOn Sound Circuit







KeyPad & LED Circuit

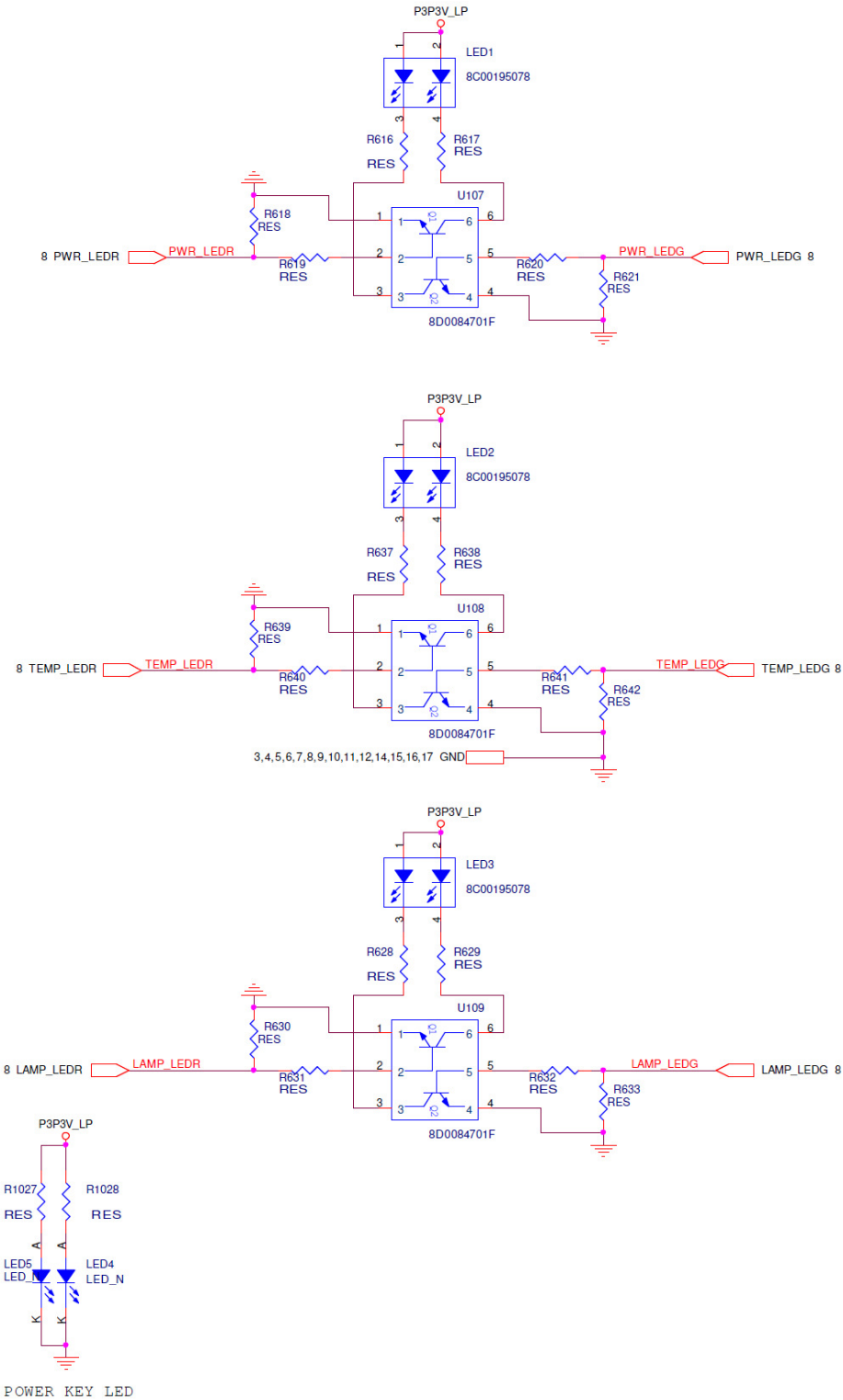


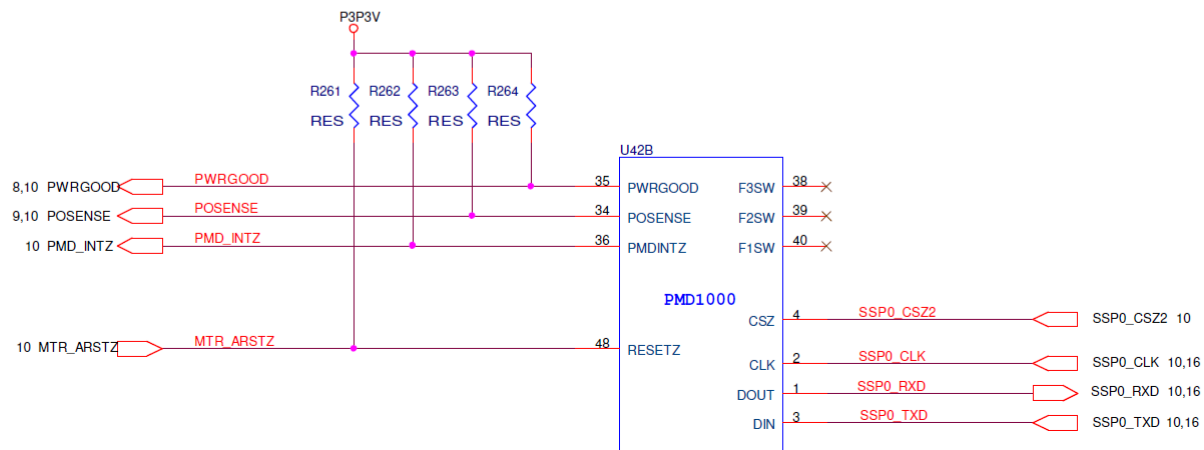
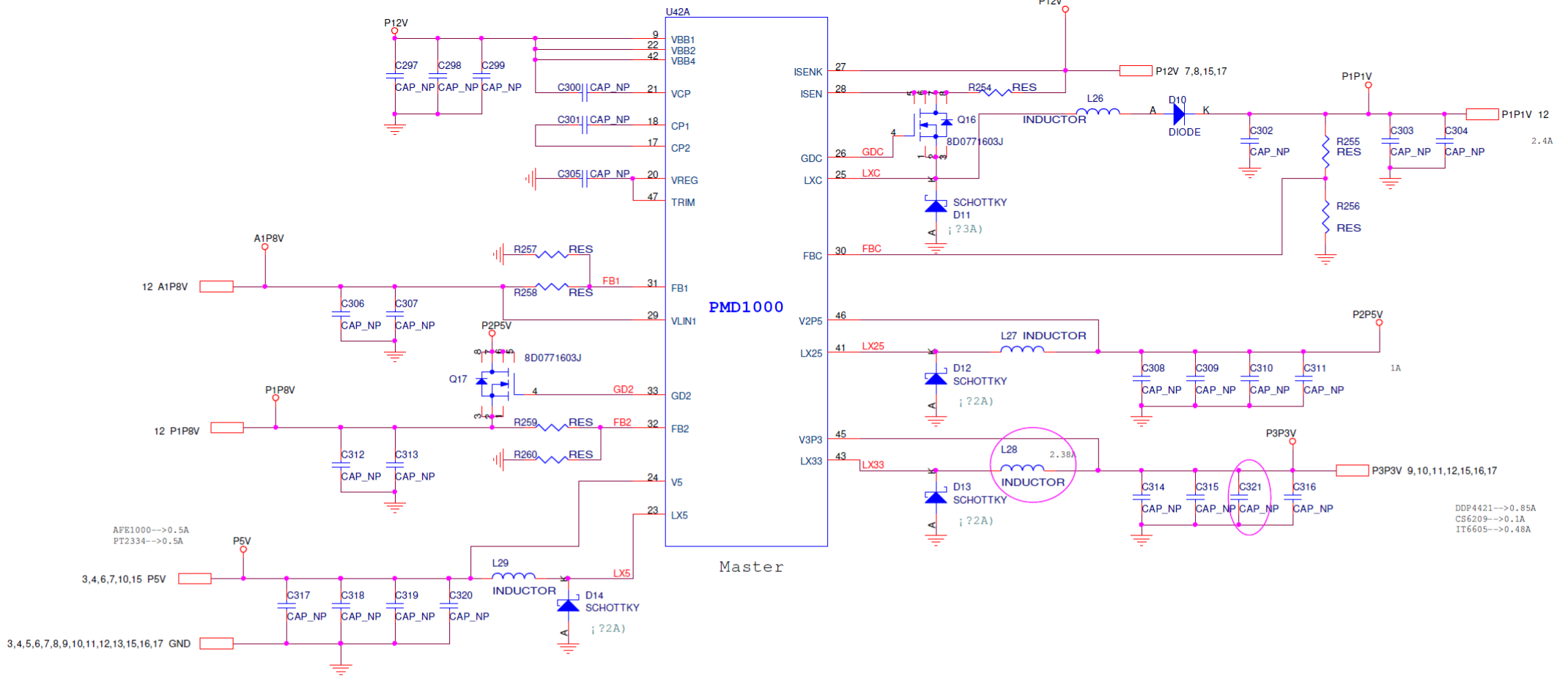
A
0~3F Menu
40~7F Up
80~BF Auto
C0~FF Releas

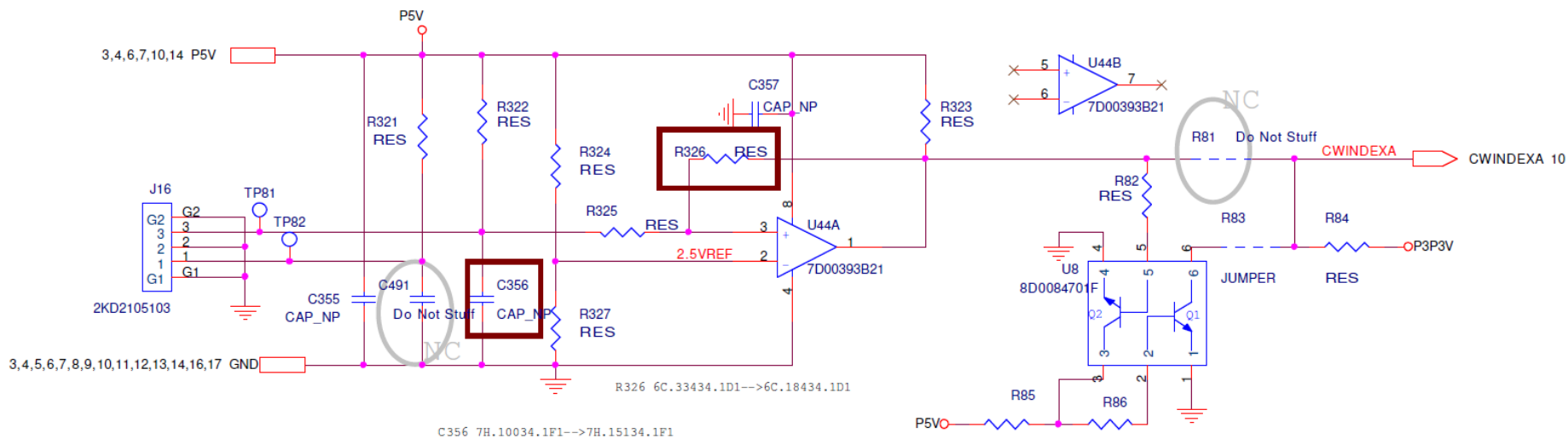
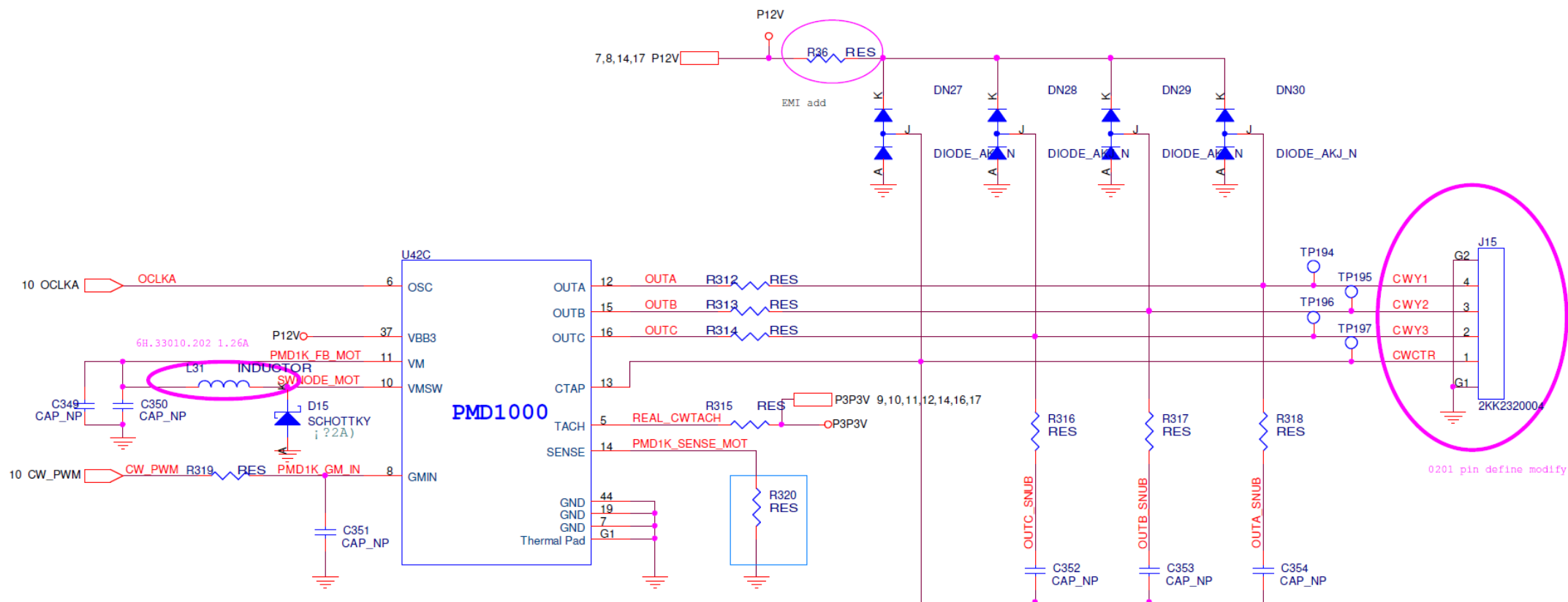
B
0~1F Left
20~5F Mode
60~9F Right
A0~DF Power
E0~FF Releas

C
0~3F Blank
40~7F Down
80~BF Source
C0~FF Releas

Base on Full Scale is equal to Vcc







Swap Ch:A with Ch:B  
Half bus port with bus data bit order swap

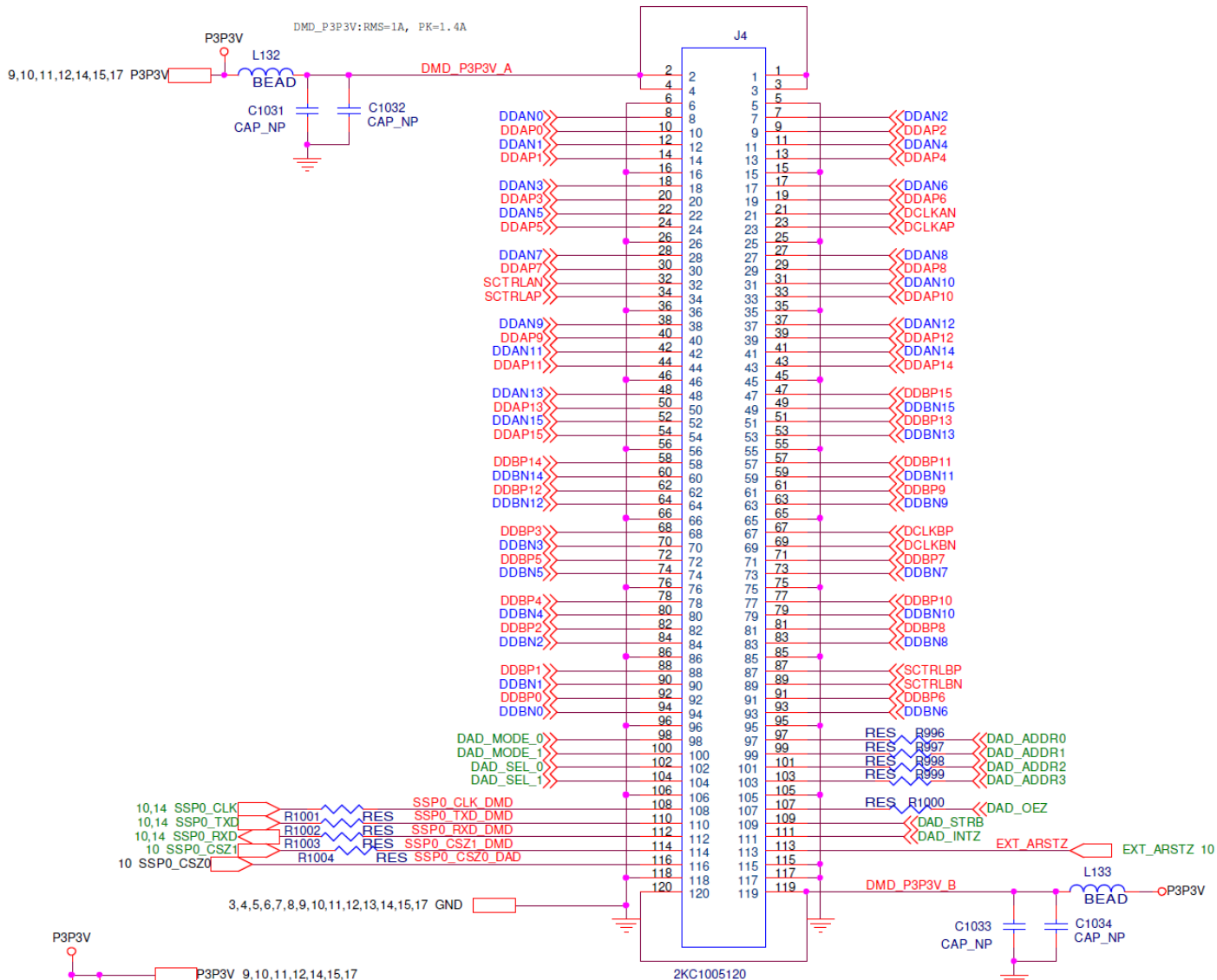
U4B

DDB\_P\_15 N1 >>DDAP15  
DDB\_N\_15 N2 >>DDAN15  
DDB\_P\_14 N3 >>DDAP14  
DDB\_N\_14 N4 >>DDAN14  
DDB\_P\_13 M2 >>DDAP13  
DDB\_N\_13 M1 >>DDAN13  
DDB\_P\_12 M3 >>DDAP12  
DDB\_N\_12 M4 >>DDAN12  
DDB\_P\_11 L1 >>DDAP11  
DDB\_N\_11 L2 >>DDAN11  
DDB\_P\_10 L3 >>DDAP10  
DDB\_N\_10 L4 >>DDAN10  
DDB\_P\_9 K1 >>DDAP9  
DDB\_N\_9 K2 >>DDAN9  
DDB\_P\_8 K3 >>DDAP8  
DDB\_N\_8 K4 >>DDAN8  
DDB\_P\_7 H1 >>DDAP7  
DDB\_N\_7 H2 >>DDAN7  
DDB\_P\_6 H3 >>DDAP6  
DDB\_N\_6 H4 >>DDAN6  
DDB\_P\_5 G1 >>DDAP5  
DDB\_N\_5 G2 >>DDAN5  
DDB\_P\_4 G3 >>DDAP4  
DDB\_N\_4 G4 >>DDAN4  
DDB\_P\_3 F1 >>DDAP3  
DDB\_N\_3 F2 >>DDAN3  
DDB\_P\_2 F3 >>DDAP2  
DDB\_N\_2 F4 >>DDAN2  
DDB\_P\_1 E1 >>DDAP1  
DDB\_N\_1 E2 >>DDAN1  
DDB\_P\_0 D1 >>DDAP0  
DDB\_N\_0 D2 >>DDAN0

DDA\_P\_15 P3 >>DDBP15  
DDA\_N\_15 P2 >>DDBN15  
DDA\_P\_14 P1 >>DDBP14  
DDA\_N\_14 P4 >>DDBN14  
DDA\_P\_13 R3 >>DDBP13  
DDA\_N\_13 R2 >>DDBN13  
DDA\_P\_12 R1 >>DDBP12  
DDA\_N\_12 T4 >>DDBN12  
DDA\_P\_11 T3 >>DDBP11  
DDA\_N\_11 T2 >>DDBN11  
DDA\_P\_10 T1 >>DDBP10  
DDA\_N\_10 U4 >>DDBN10  
DDA\_P\_9 U3 >>DDBP9  
DDA\_N\_9 U2 >>DDBN9  
DDA\_P\_8 U1 >>DDBP8  
DDA\_N\_8 W4 >>DDBN8  
DDA\_P\_7 W3 >>DDBP7  
DDA\_N\_7 W2 >>DDBN7  
DDA\_P\_6 W1 >>DDBP6  
DDA\_N\_6 Y2 >>DDBN6  
DDA\_P\_5 Y1 >>DDBP5  
DDA\_N\_5 Y4 >>DDBN5  
DDA\_P\_4 Y3 >>DDBP4  
DDA\_N\_4 AA2 >>DDBN4  
DDA\_P\_3 AA1 >>DDBP3  
DDA\_N\_3 AA4 >>DDBN3  
DDA\_P\_2 AA3 >>DDBP2  
DDA\_N\_2 AB2 >>DDBN2  
DDA\_P\_1 AB1 >>DDBP1  
DDA\_N\_1 AC2 >>DDBN1  
DDA\_P\_0 AC1 >>DDBP0  
DDA\_N\_0 AC1 >>DDBN0

DCKB\_P J3 >>DCLKAP  
DCKB\_N J4 >>DCLKAN  
DCKA\_P V4 >>DCLKBP  
DCKA\_N V3 >>DCLKBN  
SCB\_P J1 >>SCTRLAP  
SCB\_N J2 >>SCTRLAN  
SCA\_P V2 >>SCTRLBP  
SCA\_N V1 >>SCTRLBN

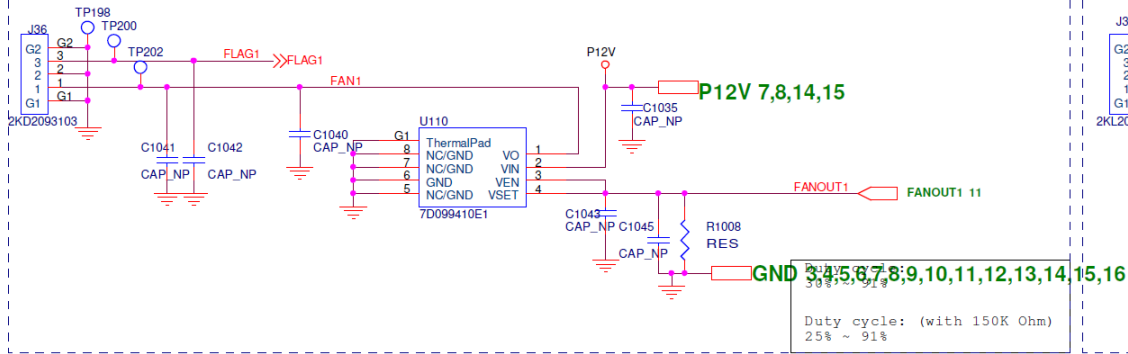
DADADDR\_0 AB8 >>DAD\_ADDR0  
DADADDR\_1 AE4 >>DAD\_ADDR1  
DADADDR\_2 AE5 >>DAD\_ADDR2  
DADADDR\_3 AD6 >>DAD\_ADDR3  
DADMODE\_0 AE6 >>DAD\_MODE\_0  
DADMODE\_1 AD7 >>DAD\_MODE\_1  
DADSEL\_0 AC7 >>DAD\_SEL\_0  
DADSEL\_1 AE4 >>DAD\_SEL\_1  
DADSTRB AE5 >>DAD\_STRB  
DADOEZ AE7 >>DAD\_OEZ  
DAD\_INTZ AC8 >>DAD\_INTZ



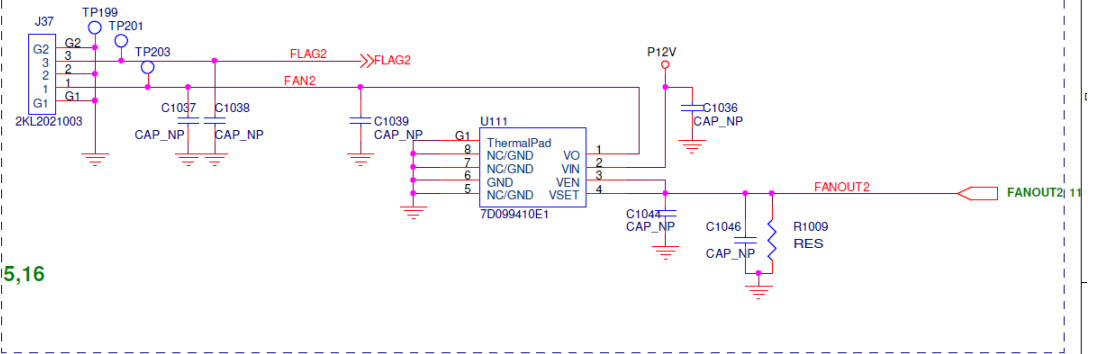
DMD INTERFACE

7A0442200J

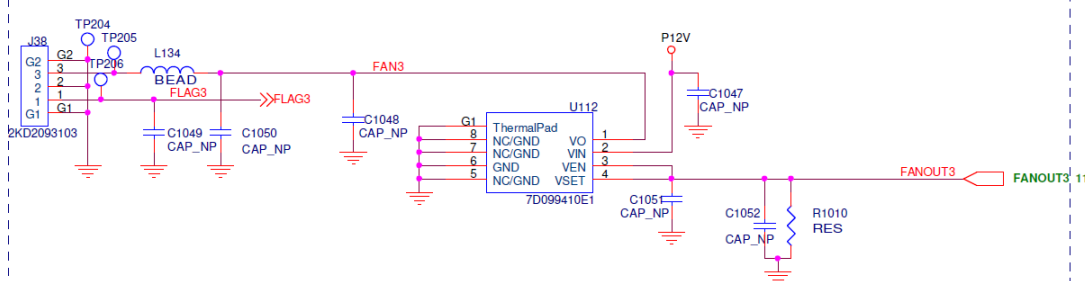
### Lamp Fan 1



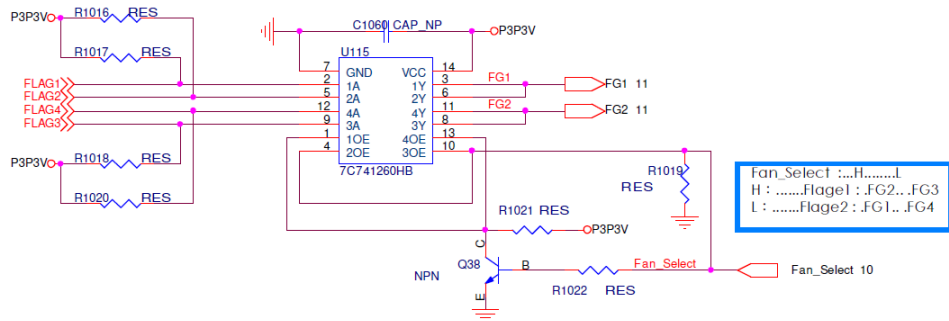
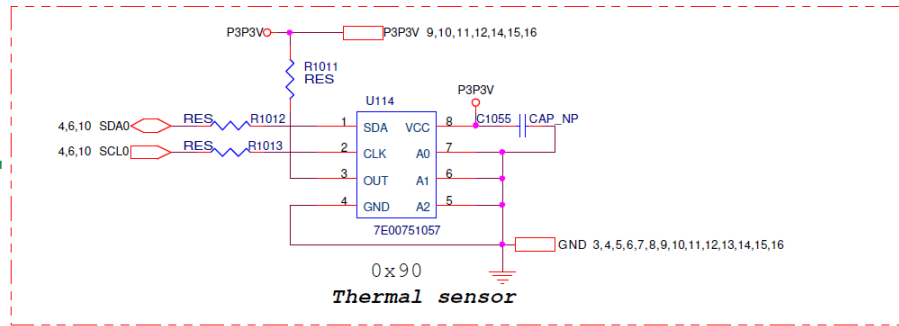
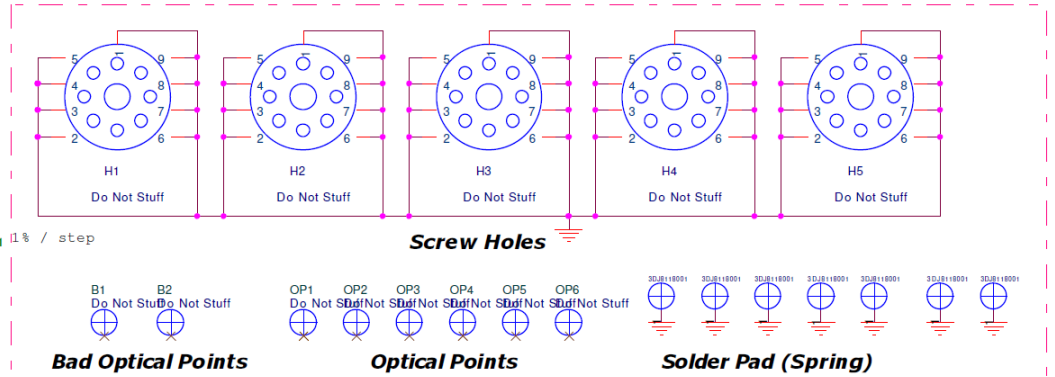
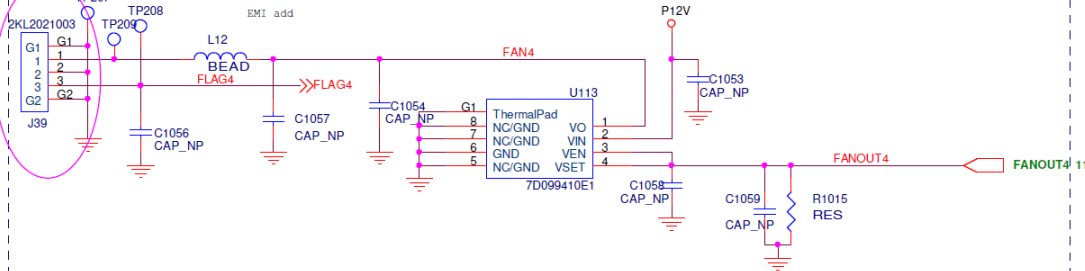
### Power Fan2



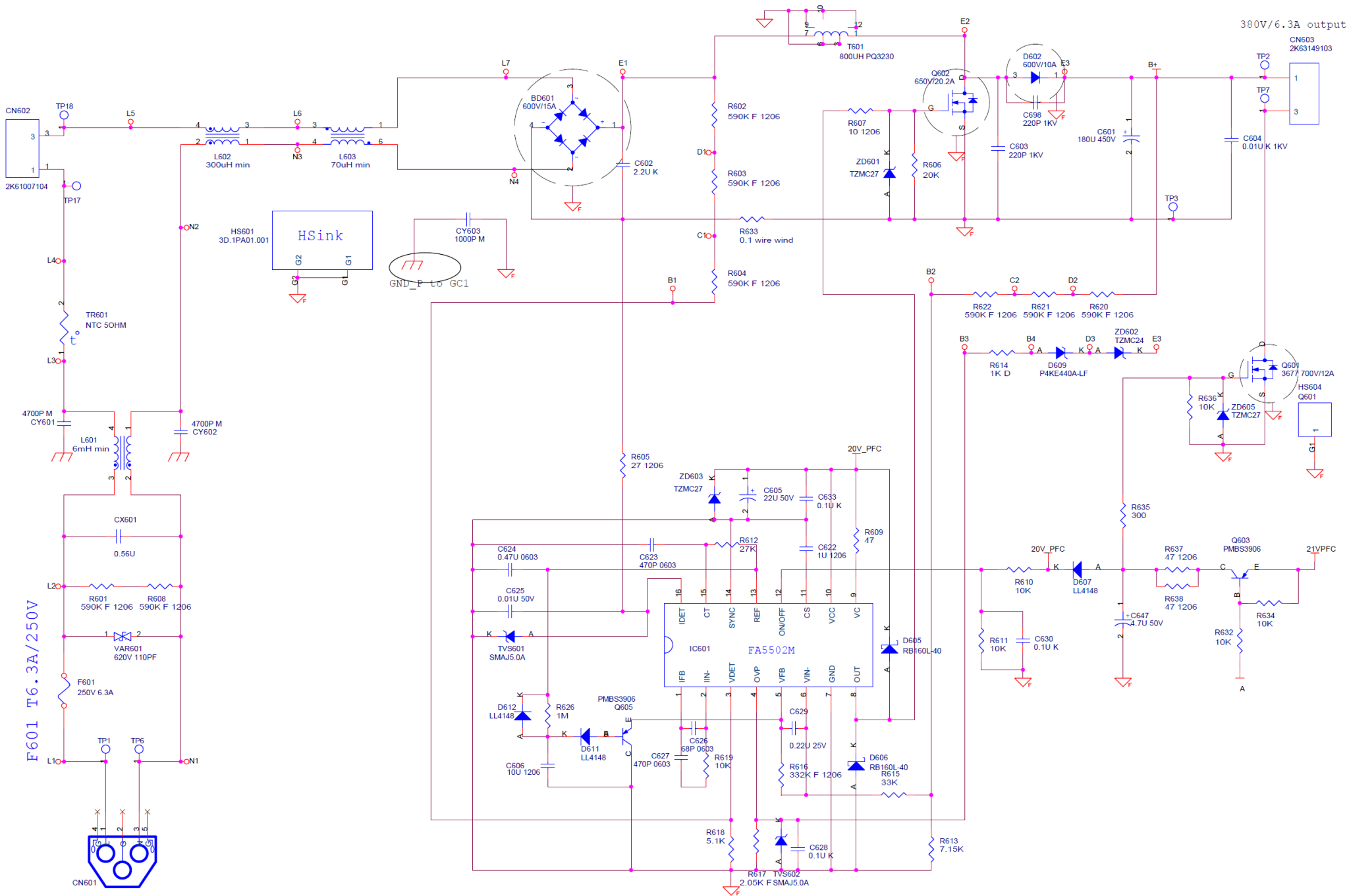
### Blower Fan 3



### Inlet Fan 4



Power board:



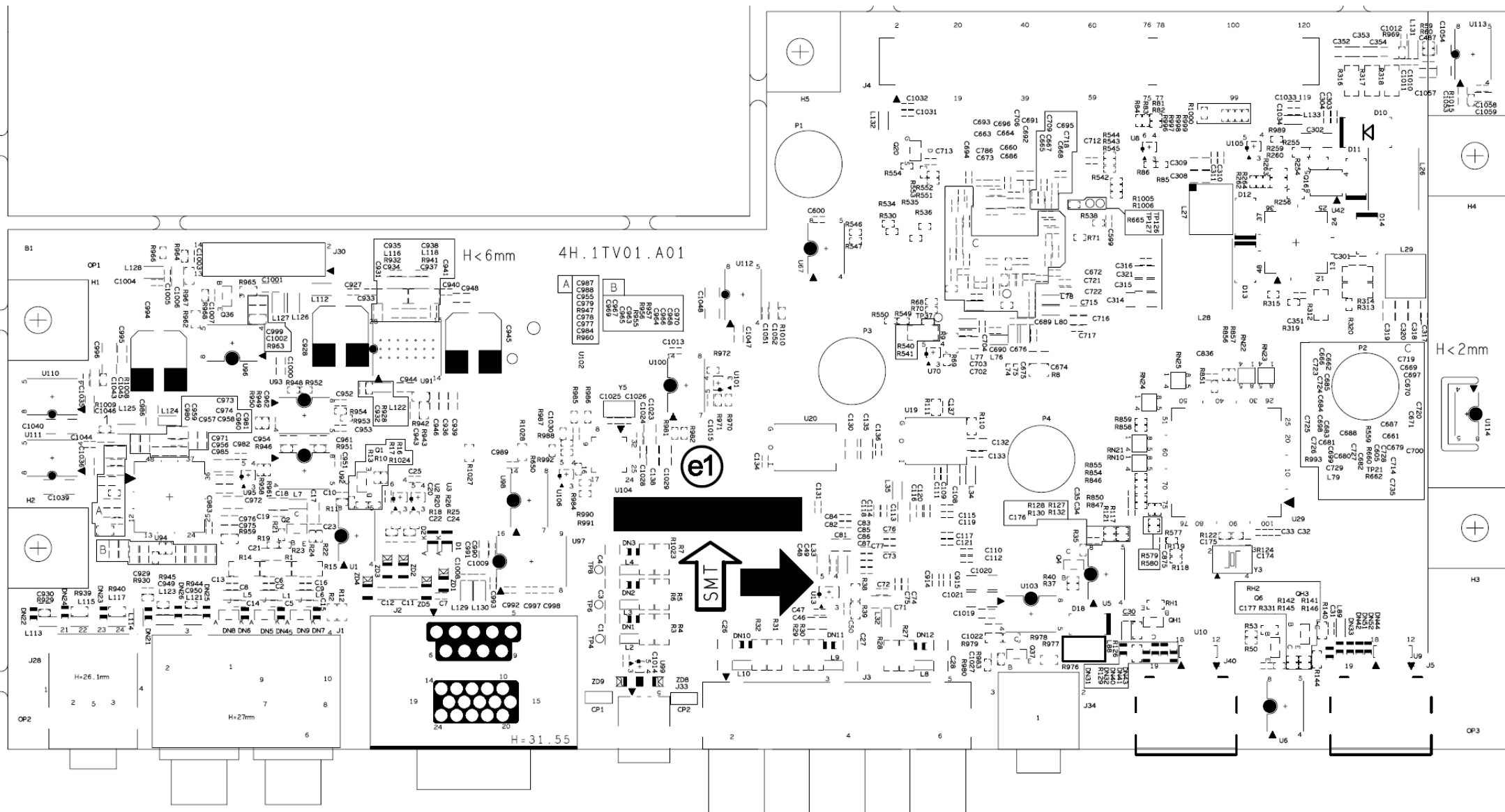
Erth Pin connector to iron case from Y/G wire.

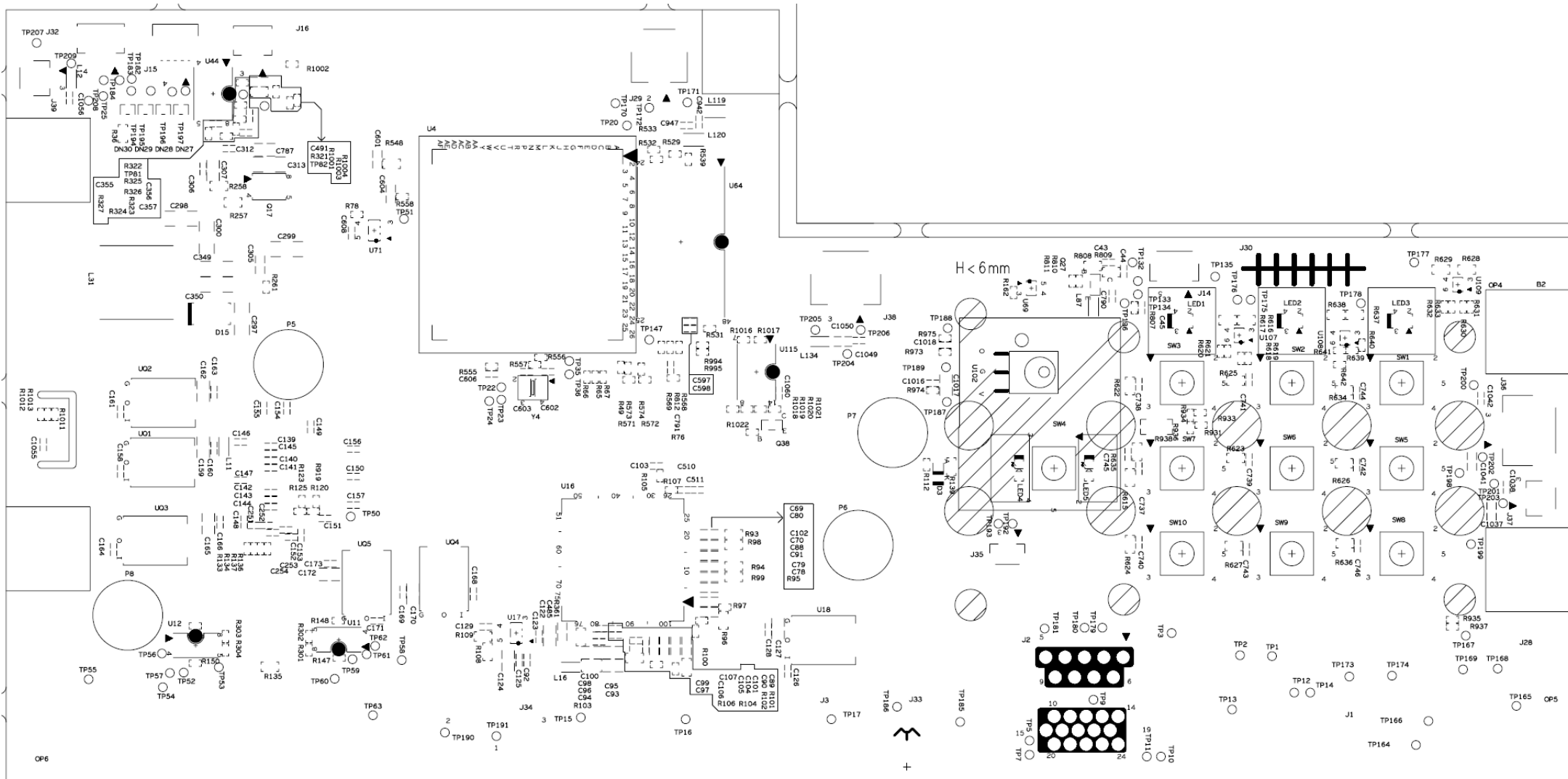
N.C : No Component





## PCB Artwork: Main Board





## Power Board

